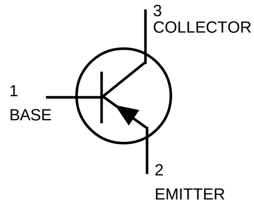
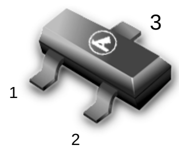
General Purpose Transistors

PNP Silicon

BCW61BLT1
BCW61CLT1
BCW61DLT1





CASE 318–08, STYLE 6
SOT–23 (TO–236AB)

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CEO}	– 32	Vdc
Collector–Base Voltage	V_{CBO}	– 32	Vdc
Emitter–Base Voltage	V_{EBO}	– 5.0	Vdc
Collector Current — Continuous	I_C	– 100	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR– 5 Board, (1) T_A = 25°C	P_D	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (2) T_A = 25°C	P_D	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T_J , T_{stg}	–55 to +150	°C

DEVICE MARKING

BCW61BLT1 = BB, BCW61CLT1 = BC, BCW61DLT1 = BD

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage (I_C = –2.0 mAdc, I_B = 0)	$V_{(BR)CEO}$	– 32	—	Vdc
Emitter–Base Breakdown Voltage (I_E= –1.0 µAdc, I_C = 0)	$V_{(BR)EBO}$	– 5.0	—	Vdc
Collector Cutoff Current	I_{CES}			
(V_{CE} = –32 Vdc,)		—	–20	nAdc
(V_{CE} = –32 Vdc, T_A = 150°C)		—	–20	µAdc

1. FR– 5 = 1.0 x 0.75 x 0.062 in.
2. Alumina = 0.4 x 0.3 x 0.024 in. 99.5% alumina.

BCW61BLT1 BCW61CLT1 BCW61DLT1

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS					
DC Current Gain		h_{FE}			—
(I_C= – 10 µAdc, V_{CE} = – 5.0 Vdc)	BCW61B		30	—	
	BCW61C		40	—	
	BCW61D		100	—	
(I_C= – 2.0 mAdc, V_{CE} = – 5.0 Vdc)		h_{FE}			—
	BCW61B		140	310	
	BCW61C		250	460	
	BCW61D		380	630	
(I_C= – 50 mAdc, V_{CE} = – 1.0 Vdc)		h_{FE}			—
	BCW61B		80	—	
	BCW61C		100	—	
	BCW61D		100	—	
AC Current Gain		h_{FE}			—
(V_{CE} = – 5.0Vdc, I_C= – 2.0 mAdc,	BCW61B		175	350	
f= 1.0 kHz)	BCW61C		250	500	
	BCW61D		350	700	
Collector–Emitter Saturation Voltage		$V_{CE(sat)}$			Vdc
(I_C = – 50 mAdc, I_B = – 1.25 mAdc)			—	– 0.55	
(I_C = – 10 mAdc, I_B = – 0.25 mAdc)			—	– 0.25	
Base–Emitter Saturation Voltage		$V_{BE(sat)}$			Vdc
(I_C = – 50 mAdc, I_B = – 1.25 mAdc)			– 0.68	– 1.05	
(I_C = – 10 mAdc, I_B = – 0.25 mAdc)			–0.6	– 0.85	
Base–Emitter On Voltage		$V_{BE(on)}$			Vdc
(I_C = – 2.0 mAdc, V_{CE} = – 5.0 Vdc)			– 0.6	– 0.75	
SMSMALL–SIGNAL CHARACTERISTICS					
Output Capacitance (V_{CE} = – 10 Vdc, I_C = 0, f = 1.0 MHz)		C_{obo}	—	6.0	pF
Noise Figure (V_{CE} = – 5.0 Vdc, I_C = – 0.2 mAdc, R_S = 2.0 kΩ, f = 1.0 kHz, BW = 200 Hz)		NF	—	6.0	dB
SWITCHING CHARACTERISTICS					
Turn–On Time (I_C = – 10 mAdc, I_{B1} = – 1.0 mAdc)		t_{on}	—	150	ns
Turn–Off Time (I_{B2} =– 1.0 mAdc, V_{BB} = – 3.6 Vdc, R_1 = R_2 = 5.0 kΩ, R_L = 990 Ω)		t_{off}	—	800	ns

BCW61BLT1 BCW61CLT1 BCW61DLT1

TYPICAL NOISE CHARACTERISTICS

(V_{CE} = –5.0 Vdc, T_A = 25°C)

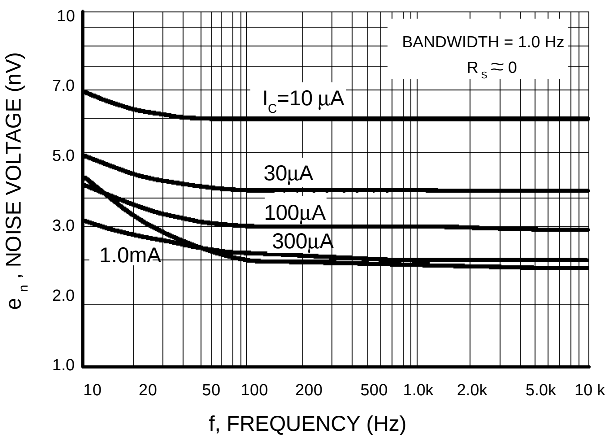


Figure 1. Noise Voltage

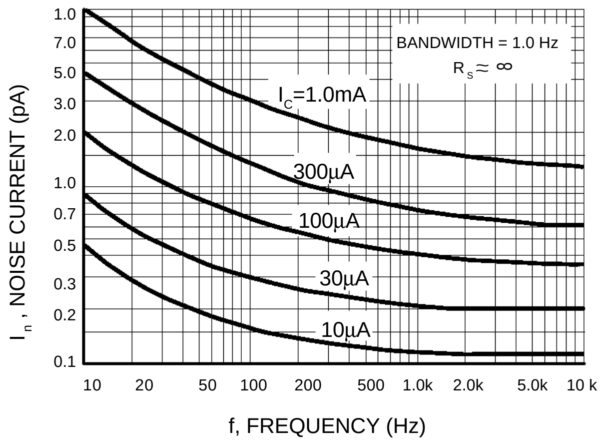


Figure 2. Noise Current

NOISE FIGURE CONTOURS

(V_{CE} = –5.0 Vdc, T_A = 25°C)

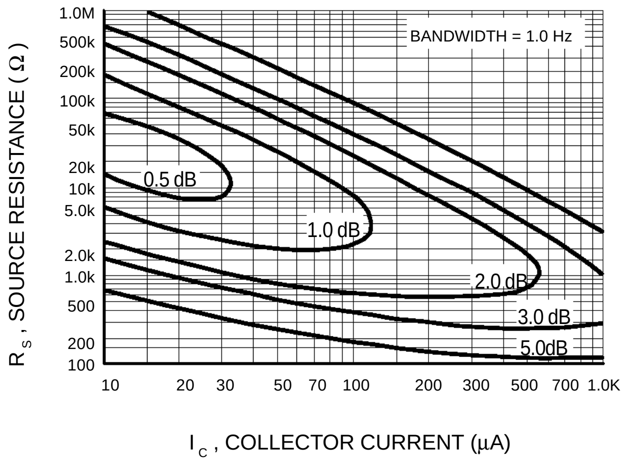


Figure 3. Narrow Band, 100 Hz

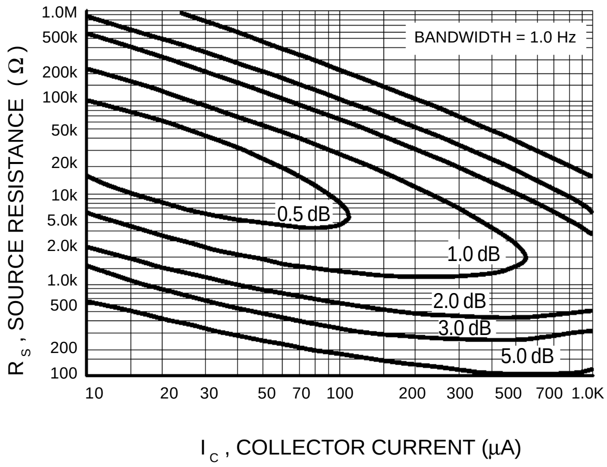


Figure 4. Narrow Band, 1.0 kHz

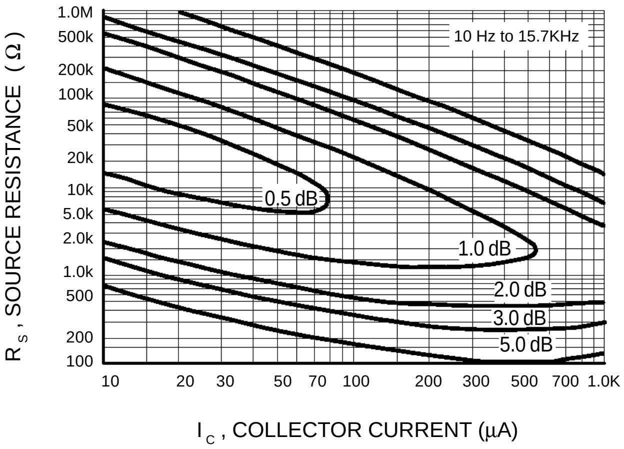


Figure 5. Wideband

Noise Figure is Defined as:

$$NF = 20 \log_{10} \left(\frac{e_n^2 + 4KTR_S + I_n^2 R_S^2}{4KTR_S} \right)^{1/2}$$

e_n = Noise Voltage of the Transistor referred to the input. (Figure 3)
I_n = Noise Current of the Transistor referred to the input. (Figure 4)
K = Boltzman's Constant (1.38 x 10^{-23} j/°K)
T = Temperature of the Source Resistance (°K)
R_s = Source Resistance (Ω)

BCW61BLT1 BCW61CLT1 BCW61DLT1

TYPICAL STATIC CHARACTERISTICS

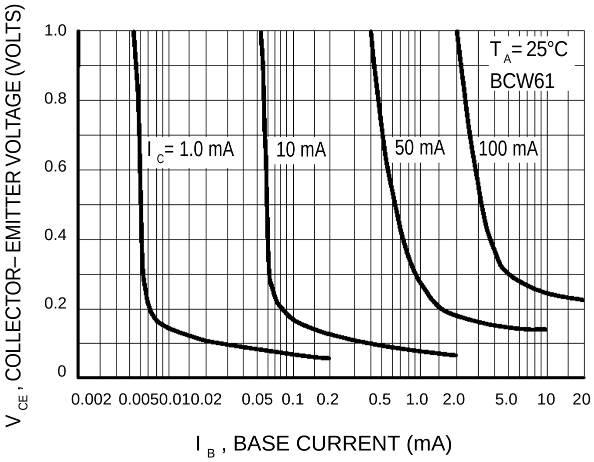


Figure 6. Collector Saturation Region

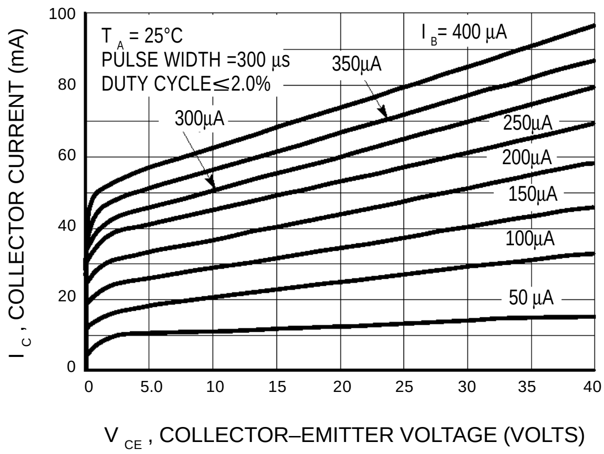


Figure 7. Collector Characteristics

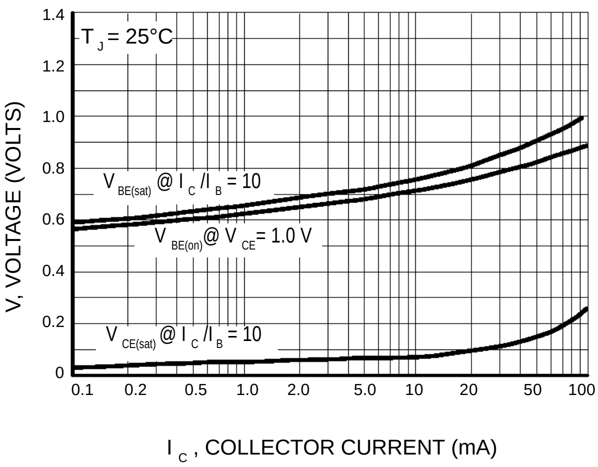


Figure 8. “On” Voltages

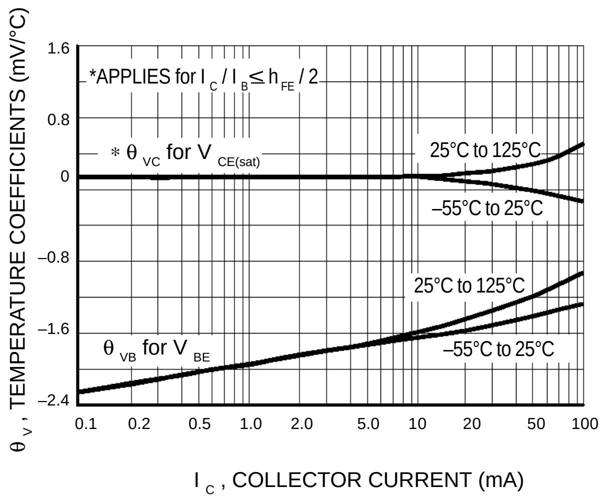


Figure 9. Temperature Coefficients

BCW61BLT1 BCW61CLT1 BCW61DLT1

TYPICAL DYNAMIC CHARACTERISTICS

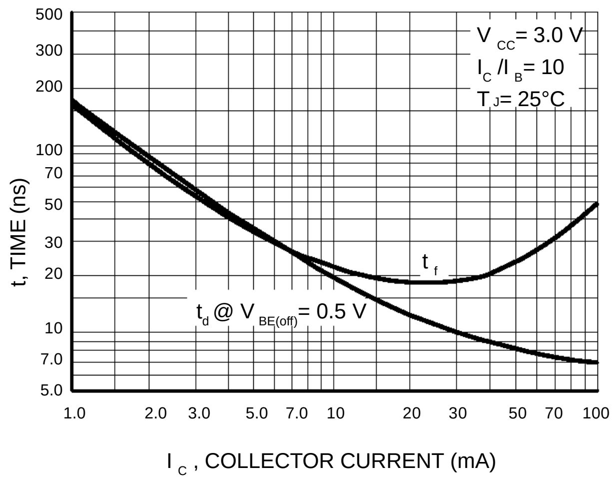


Figure 10. Turn–On Time

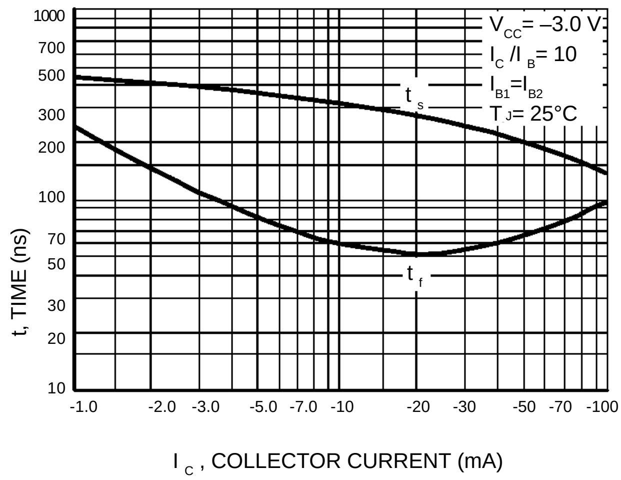


Figure 11. Turn–Off Time

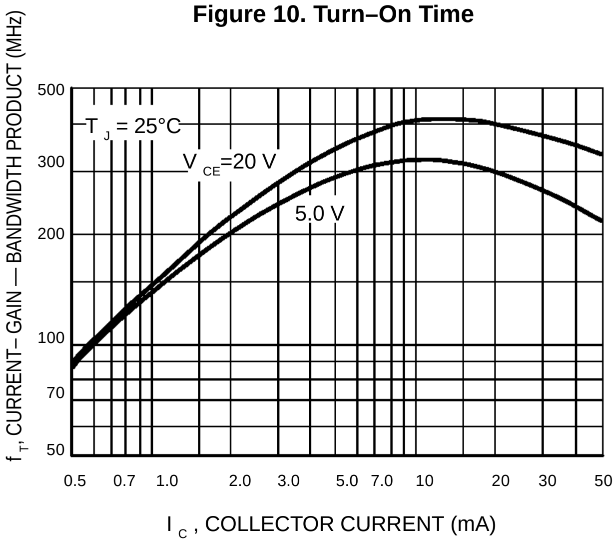


Figure 12. Current–Gain — Bandwidth Product

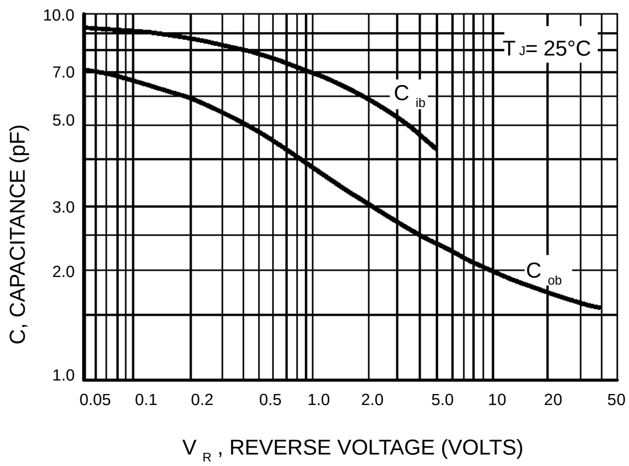


Figure 13. Capacitance

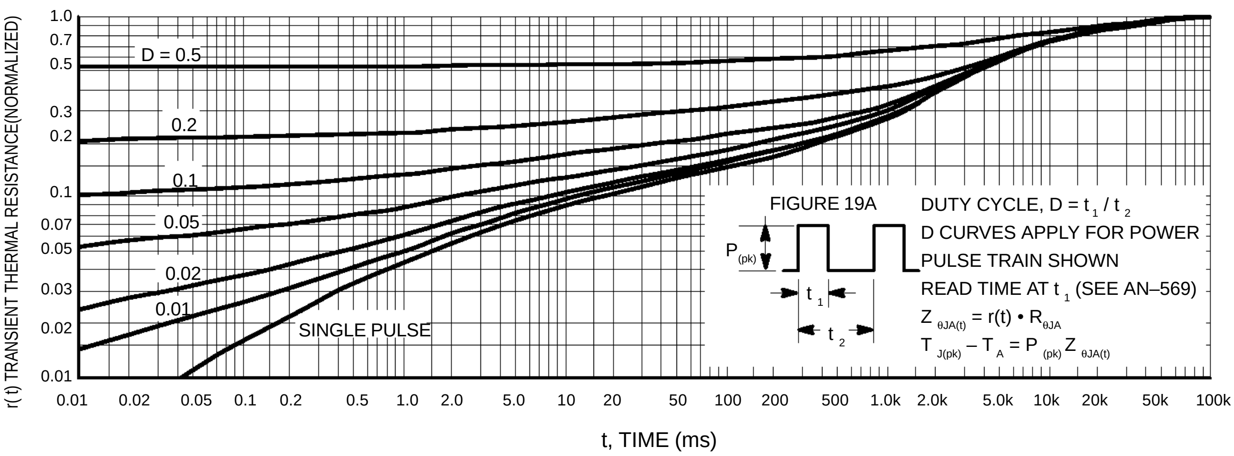


Figure 14. Thermal Response

BCW61BLT1 BCW61CLT1 BCW61DLT1

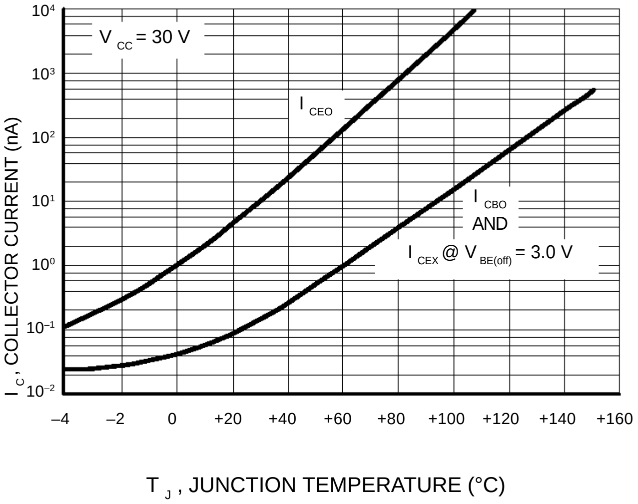


Figure 15. Typical Collector Leakage Current

DESIGN NOTE: USE OF THERMAL RESPONSE DATA

A train of periodical power pulses can be represented by the model as shown in Figure 15. Using the model and the device thermal response the normalized effective transient thermal resistance of Figure 14 was calculated for various duty cycles.

To find $Z_{\theta JA(t)}$, multiply the value obtained from Figure 14 by the steady state value $R_{\theta JA}$.

Example:

The MPS3905 is dissipating 2.0 watts peak under the following conditions:

t_1 = 1.0 ms, t_2 = 5.0 ms. (D = 0.2)

Using Figure 14 at a pulse width of 1.0 ms and D = 0.2, the reading of r(t) is 0.22.

The peak rise in junction temperature is therefore

$\Delta T = r(t) \times P_{(pk)} \times R_{\theta JA} = 0.22 \times 2.0 \times 200 = 88°C$.

For more information, see AN–569.